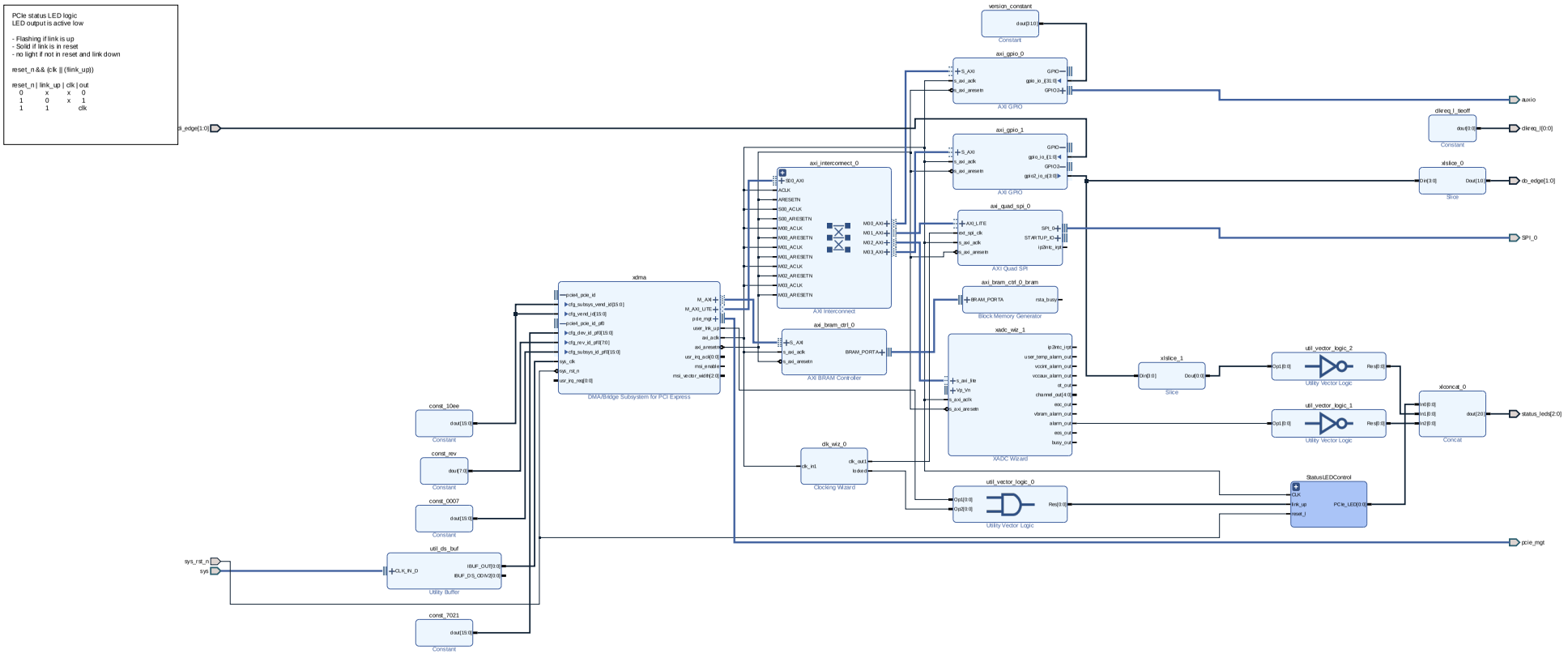
PCIe status LED logic
LED output is active low
- Flashing if link is up
- Solid if link is in reset
- no light if not in reset and link down
reset_n && (clk || (!link_up))
reset_n | link_up | clk | out
0 x x 0
1 0 x 1
1 1 clk
db_edge[1:0]
version_constant
dout[1:0]
Constant
axi_gpio_0
S_AXI
s_axi_aclk
s_axi_aresetn
GPIO
gpio_io_i[31:0]
GPIO2
AXI GPIO
auxio
chireq_i_tieoff
dout[0:0]
Constant
chireq_i[0:0]
axi_gpio_1
gpio_io_i[1:0]
GPIO2
gpio2_io_i[3:0]
xlslice_0
Din[3:0]
Dout[1:0]
Slice
db_edge[1:0]
axi_interconnect_0
S00_AXI
ACLK
ARESETN
S00_ACLK
S00_ARESETN
M00_ACLK
M00_ARESETN
M01_ACLK
M01_ARESETN
M02_ACLK
M02_ARESETN
M03_ACLK
M03_ARESETN
M00_AXI
M01_AXI
M02_AXI
M03_AXI
AXI Interconnect
axi_quad_spi_0
AXI_LITE
ext_spi_clk
s_axi_aclk
s_axi_aresetn
SPI_0
STARTUP_IO
ip2intc_irpt
AXI Quad SPI
SPI_0
xdma
pcie_mgt
cfg_subsys_vend_id[15:0]
cfg_vend_id[15:0]
pcie_pcie_id_ptl
cfg_dev_id_pf0[15:0]
cfg_rev_id_pf0[7:0]
cfg_subsys_id_pf0[15:0]
sys_clk
sys_rst_n
usr_irq_req[0:0]
M_AXI
M_AXI_LITE
pcie_mgt
user_lnk_up
axi_aclk
axi_aresetn
usr_irq_ack[0:0]
msi_enable
msi_vector_width[2:0]
DMA/Bridge Subsystem for PCI Express
axi_bram_ctrl_0_bram
BRAM_PORTA
rsta_busy
Block Memory Generator
axi_bram_ctrl_0
S_AXI
s_axi_aclk
s_axi_aresetn
BRAM_PORTA
AXI BRAM Controller
xadc_wiz_1
s_axi_lite
Vp_Vn
s_axi_aclk
s_axi_aresetn
ip2intc_irpt
user_temp_alarm_out
vccint_alarm_out
vccaux_alarm_out
ot_out
channel_out[4:0]
eoc_out
vbram_alarm_out
alarm_out
eos_out
busy_out
XADC Wizard
xlslice_1
Din[3:0]
Dout[0:0]
Slice
util_vector_logic_2
Op1[0:0]
Res[0:0]
Utility Vector Logic
util_vector_logic_1
Op1[0:0]
Res[0:0]
Utility Vector Logic
xlconcat_0
In0[0:0]
In1[0:0]
In2[0:0]
dout[2:0]
Concat
status_leds[2:0]
const_10ee
dout[15:0]
Constant
const_rev
dout[7:0]
Constant
const_0007
dout[15:0]
Constant
const_7021
dout[15:0]
Constant
clk_wiz_0
clk_in1
clk_out1
locked
Clocking Wizard
util_vector_logic_0
Op1[0:0]
Op2[0:0]
Res[0:0]
Utility Vector Logic
StatusLEDControl
CLK
link_up
reset_i
PCIe_LED[0:0]
pcie_mgt
sys_rst_n
sys_clk
util_ds_buf
CLK_IN_D
IBUF_OUT[0:0]
IBUF_DS_ODIV2[0:0]
Utility Buffer